Electronic Patent Application Fee Transmittal								
Application Number:	10534962							
Filing Date:	16-May-2005							
Title of Invention:	CIRCUIT BOARD, MULTI-LAYER WIRING BOARDS, METHOD OF PRODUCING CIRCUIT BOARDS AND METHOD OF PRODUCING MULTILAYER WIRING BOARDS							
First Named Inventor/Applicant Name:	Masayoshi Kondo							
Filer:	Robert G. Weilacher/Bess Murray							
Attorney Docket Number:	033036.088							
Filed as Large Entity								
U.S. National Stage under 35 USC 371 Filing Fees								
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)			
Basic Filing:								
Pages:								
Claims:								
Miscellaneous-Filing:								
Petition:								
Patent-Appeals-and-Interference:								
Post-Allowance-and-Post-Issuance:								
Utility Appl issue fee		1501	1	1510	1510			
Publ. Fee- early, voluntary, or normal		1504	1	300	300			

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
	Total in USD (\$)			1810